



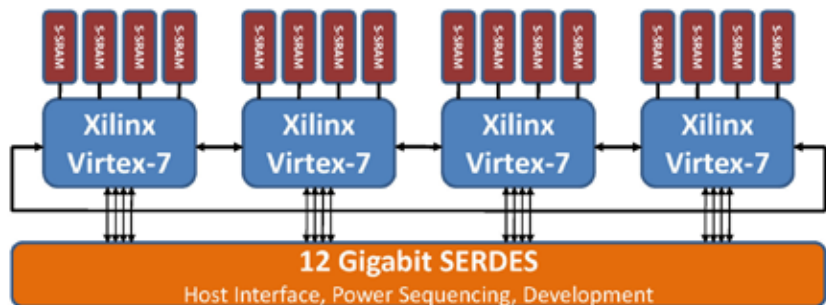
Virtex-7 FPGA processing array design from OTEC makes industrial robotic motion control possible in real time.

Xilinx Virtex-7 FPGA Processing Array

This Xilinx Virtex-7 FPGA processing array performs tera-flops of real-time calculations in a four-device processing array. Making use of Virtex-7 FPGAs from Xilinx, this FPGA processing array performs on-chip, inter-chip, tandem and stored calculations. DSP Blocks, on-chip memory, off-chip high speed static RAM, and robust inter-chip communications make this processing array extremely versatile. Rapid reprogrammability in various host-computer configurations completes the development and application structure.

12 Gigabit SerDes Communications

12 gigabit SerDes communications makes high-speed data injection and results communications possible. On-chip single, dual, quad or octally configurable SerDes links transfer data over 8b/10b encoded links with remarkable speed and error-free performance.



Power Sequencing and System Support

Xilinx FPGA power up requires careful voltage rail sequencing. OTEC brings a wealth of experience to high current, analog power system sequencing design.

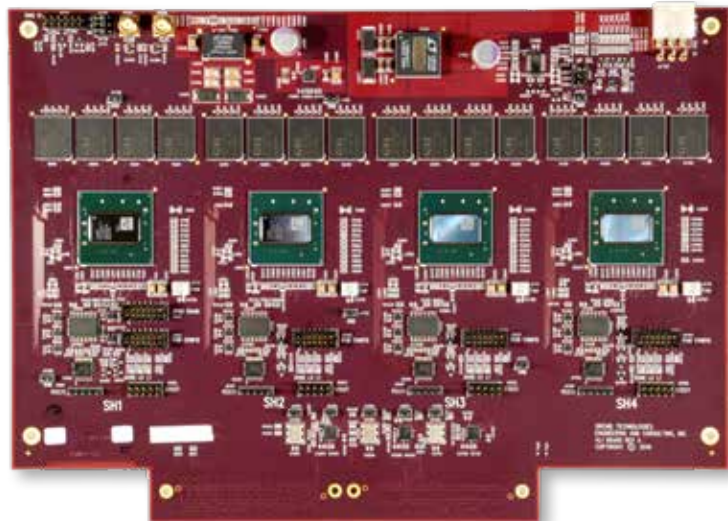


Orchid Technologies: Xilinx FPGA Design

The development of custom electronic products for our OEM clients is OTEC's entire business. The design of high performance FPGA processing arrays with rapid design cycles, demanding technical requirements, and unforgiving schedules sets us apart. Call Orchid Technologies today!

OTEC designed and delivered our complex FPGA processing array in under five months. Perfect the first time, our FPGA has worked flawlessly from day one. Outstanding!

- VP Development Engineering



**ORCHID TECHNOLOGIES
ENGINEERING & CONSULTING, INC.**

Custom Engineering From Concept to Production

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