

Algorithm development begins with ideas. Concepts are worked out with Matlab analysis and resource simulation. Final HDL implementation proceeds from there.



“Orchid helped us to understand the realities of FPGA design. Their understanding of the tools of real life algorithm development was invaluable.”

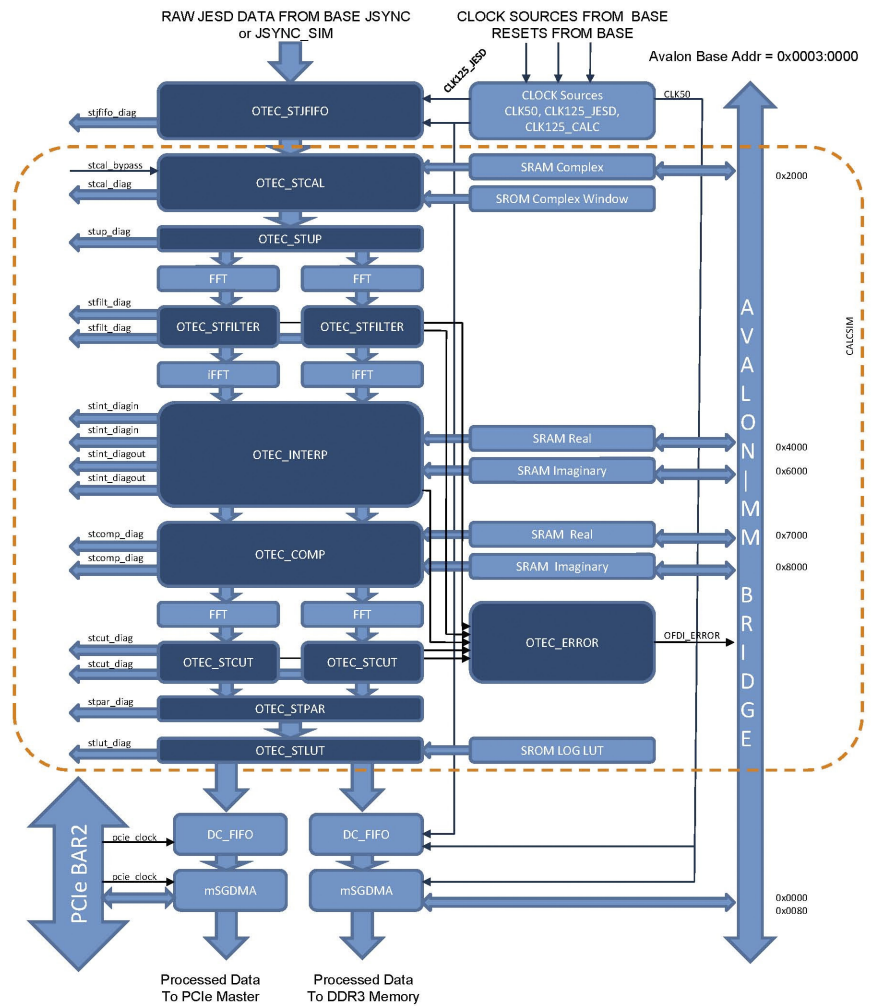
- OCT Design Manager

Custom Digital Signal Processing Design

Custom digital signal processing design is as much an art as a science. Orchid’s experience with the design, simulation and implementation of complex signal processing algorithms runs deep. Knit together with the industry standard Avalon bus, Orchid has implemented multistage algorithms which include FFT, FIR Filter, scaling, complex number mathematics, dynamic resizing, rectilinear, color space conversion stages and more. Orchid’s modular approach permits algorithm validation at each step.

Matlab, Modelsim, Verilog Design Expertise

MATLAB system design, Modelsim Simulation, Verilog and VHDL implementation combine to produce a working, hand coded efficient FPGA algorithm. Pipelined design techniques insure operating speed goals are met, while keeping the design area efficient. Timing closure wraps up the overall implementation for clean compilations time and time again.



Orchid Technologies: FPGA Algorithm Design

The development of custom electronic products for our OEM clients is Orchid’s entire business. The design of high speed custom FPGA algorithms with rapid design cycles, demanding technical requirements, and unforgiving schedules sets us apart. Call Orchid Technologies today!

